

FIG. 1

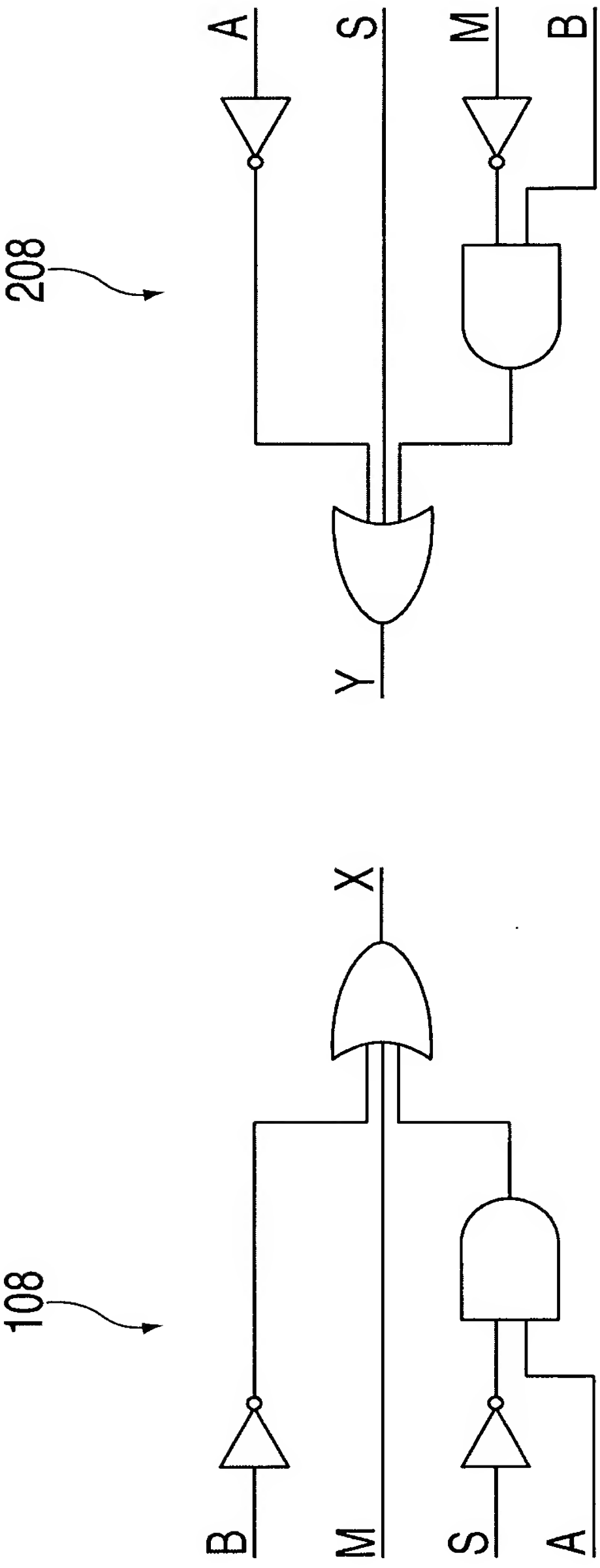


FIG. 2A

FIG. 2B

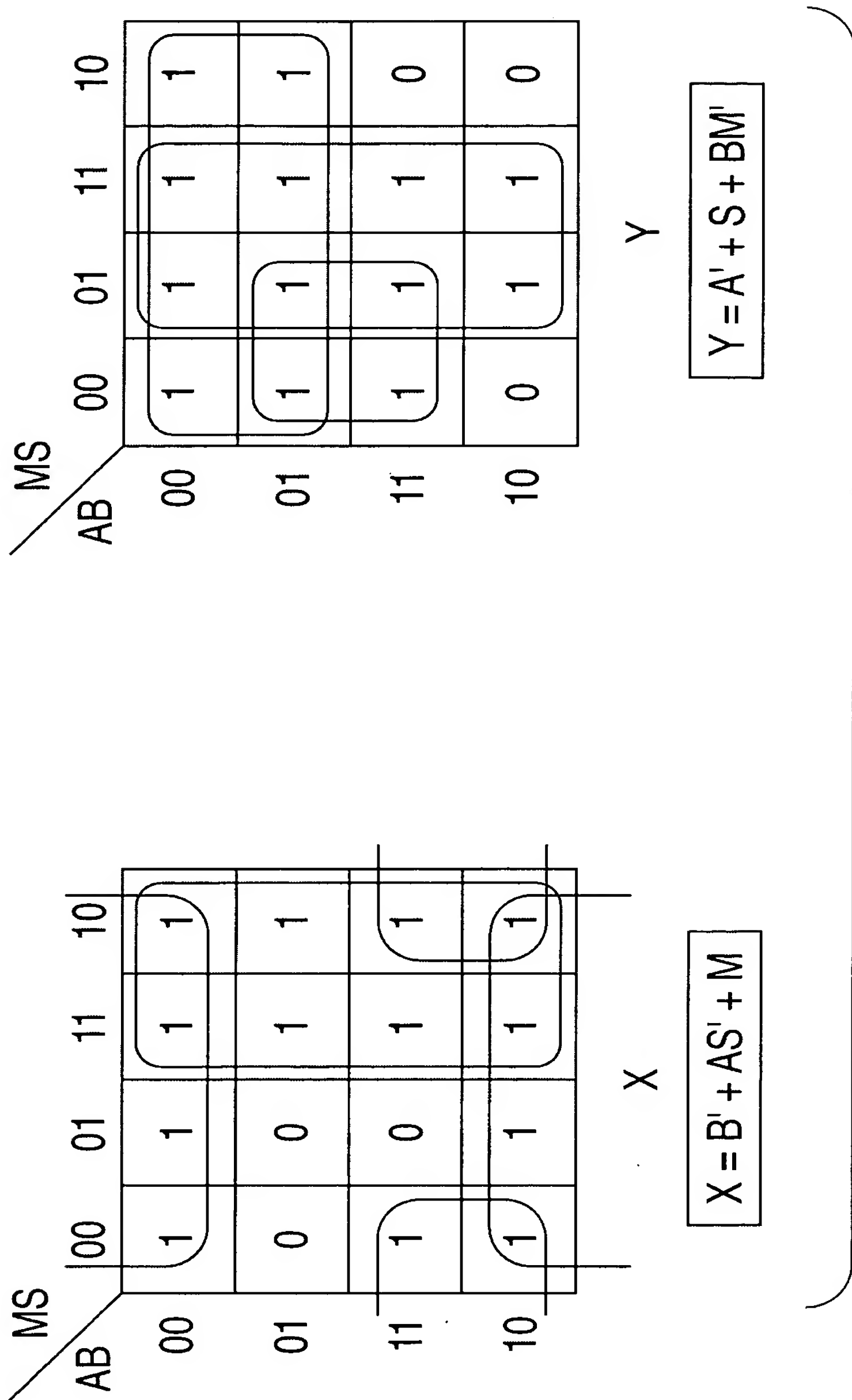
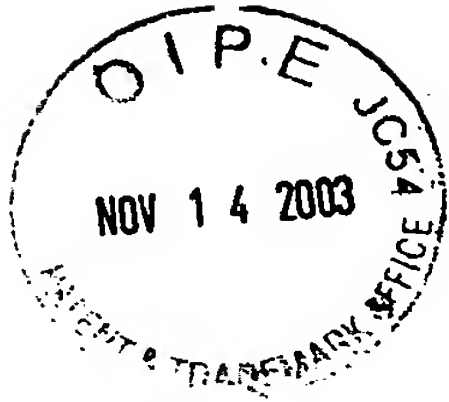
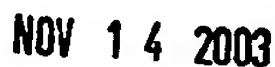
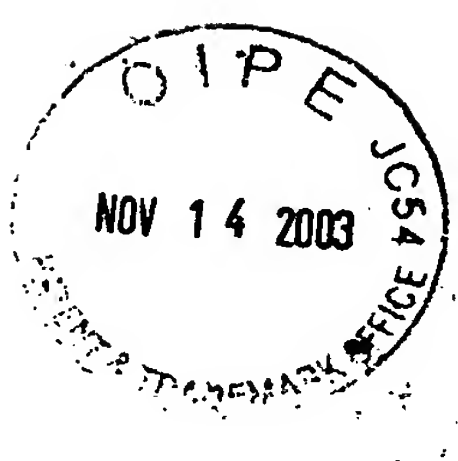


FIG. 3



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PRESENT STATE			INPUTS		NEXT STATE	
A	B		M	S	X	Y
0	0		0	0	1	1
0	0		0	1	1	1
0	0		1	1	1	1
0	0		1	0	1	1
0	1		0	0	0	1
0	1		0	1	0	1
0	1		1	1	1	1
0	1		1	0	1	1
1	1		0	0	1	1
1	1		0	1	0	1
1	1		1	1	1	1
1	1		1	0	1	0
1	0		0	0	1	0
1	0		0	1	1	1
1	0		1	1	1	1
1	0		1	0	1	0



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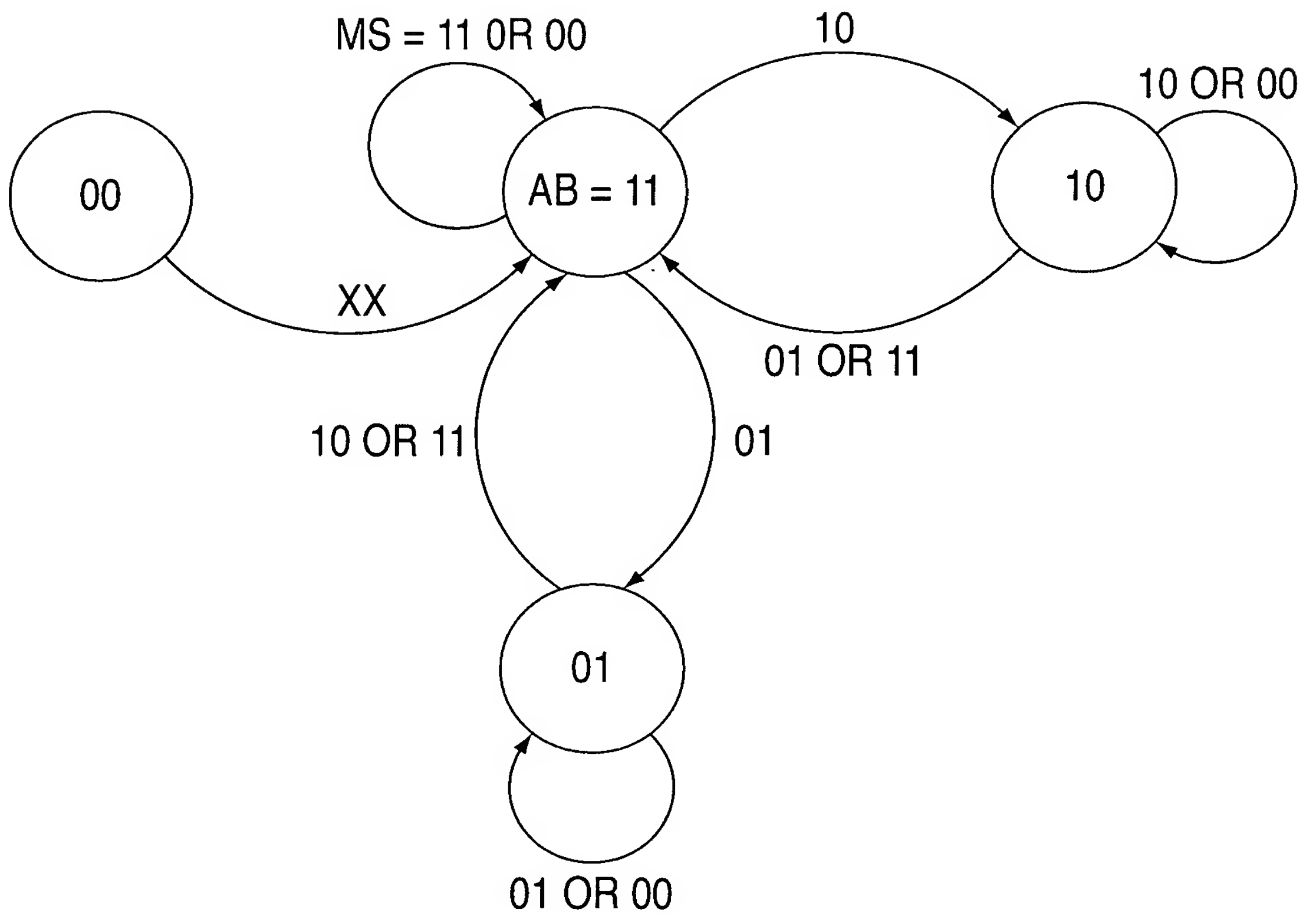


FIG. 5



TIME	EVENT	RESULT	TRANSITION TESTED
0s	START	CLOCK RUNNING. INPUTS UNDEFINED.	
52ns	MASTER DRIVES HIGH	SLAVE FOLLOWS HIGH ON NEXT CLOCK PULSE; STATE TRANSITIONS TO AB='11'	h
102ns	MASTER DRIVES LOW	SLAVE FOLLOWS LOW ON NEXT CLOCK PULSE; STATE TRANSITIONS TO AB='01'	e
152ns	MASTER DRIVES HIGH	SLAVE FOLLOWS HIGH ON NEXT CLOCK PULSE; STATE TRANSITIONS TO AB='11'	f
175ns	MASTER RELEASES.	STATE REMAINS AT AB='11'	
202ns	SLAVE DRIVES LOW	MASTER FOLLOWS LOW ON NEXT CLOCK PULSE; STATE TRANSITIONS TO AB='10'	b
252ns	SLAVE DRIVES HIGH	MASTER FOLLOWS HIGH ON NEXT CLOCK PULSE; STATE TRANSITIONS TO AB='11'	d
302ns	MASTER AND SLAVE DRIVE LOW	STATE REMAINS AT AB='11'	a
352ns	MASTER AND SLAVE DRIVE HIGH	STATE REMAINS AT AB='11'	a
375ns	SLAVE RELEASES.	STATE REMAINS AT AB='11'	
402ns	MASTER DRIVES LOW	SLAVE FOLLOWS LOW ON NEXT CLOCK PULSE; STATE TRANSITIONS TO AB='01'	e
452ns	SLAVE DRIVES HIGH	STATE REMAINS AT AB='01'	g
475ns	SLAVE RELEASES.	SLAVE FOLLOWS LOW ON NEXT CLOCK PULSE; STATE REMAINS AB='01'	g
552ns	MASTER AND SLAVE DRIVE HIGH	STATE TRANSITIONS TO AB='11'	f
625ns	MASTER RELEASES.	STATE REMAINS AT AB='11'	
652ns	SLAVE DRIVES LOW	MASTER FOLLOWS LOW ON NEXT CLOCK PULSE; STATE TRANSITIONS TO AB='10'	b
702ns	MASTER DRIVES HIGH	STATE REMAINS AT AB='10'	c
725ns	MASTER RELEASES.	MASTER FOLLOWS LOW ON NEXT CLOCK PULSE; STATE REMAINS AB='10'	c
802ns	MASTER AND SLAVE DRIVE HIGH	STATE TRANSITIONS TO AB='11'	d

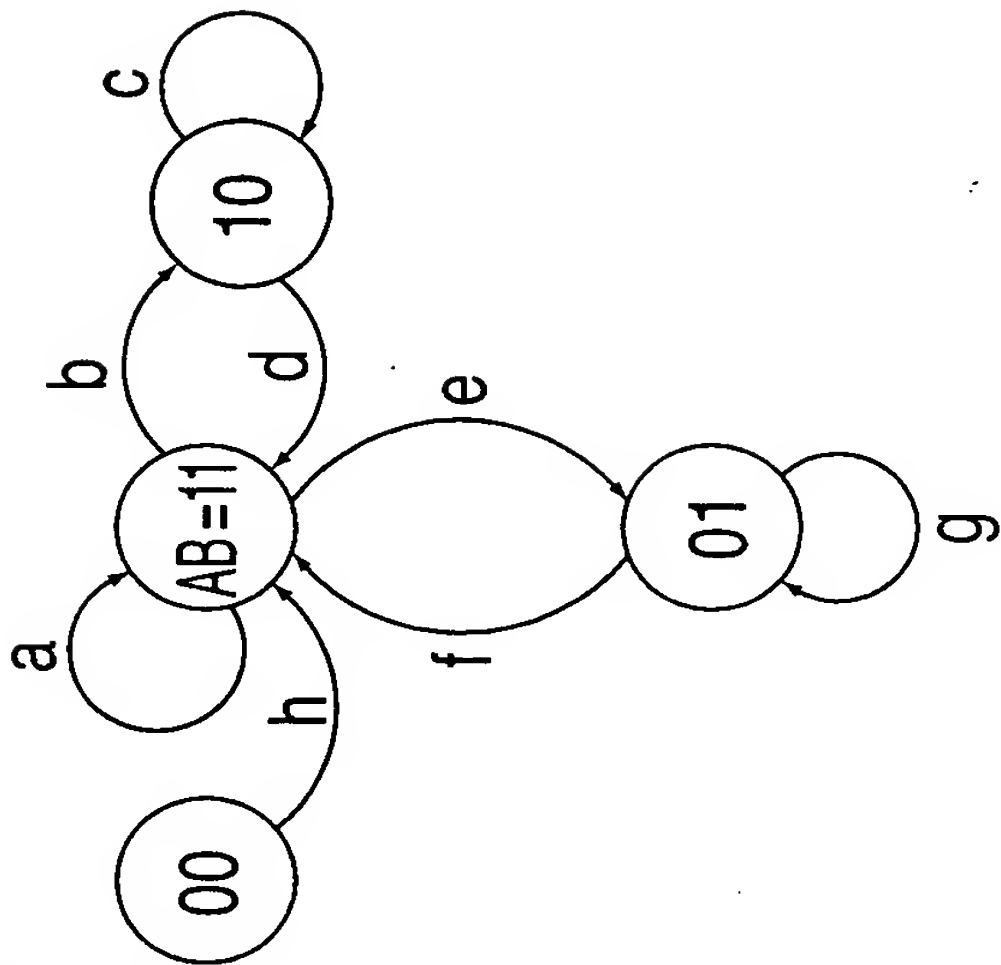


FIG. 6

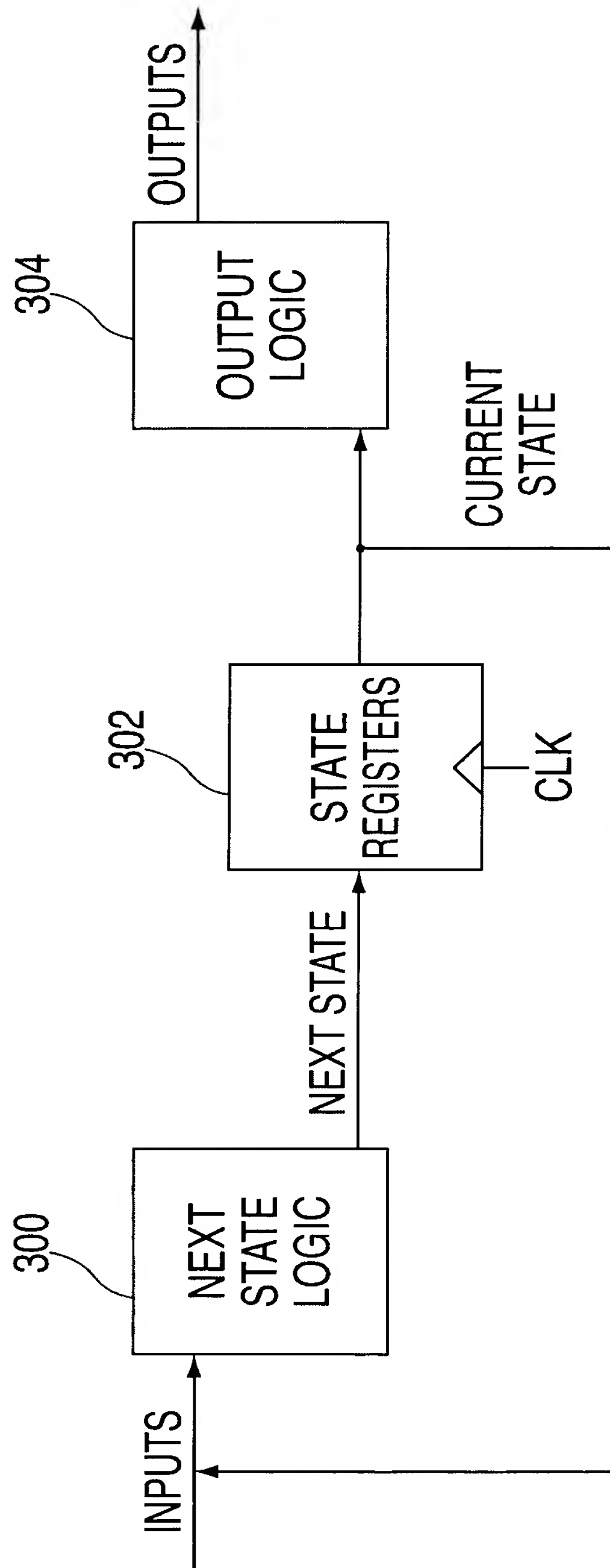
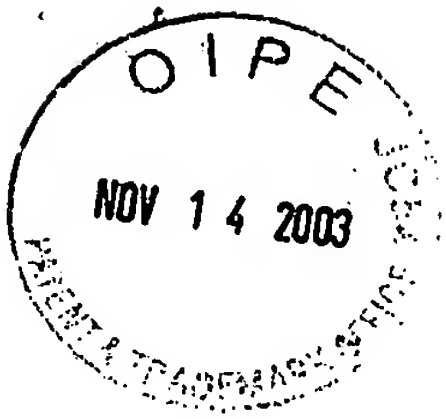


FIG. 7